PCN Number: 201				81116001.1				PC	N Da	te:	Nov 19 2018			
Title:	<b>Title:</b> Qualification of additional Fab site (RFAB) and Assembly/Test site (AP1) options for the TLV6002 device family							tions for the						
<b>Customer Contact:</b>				PCN Manager				De	pt:		Quality Services			
Proposed 1 <sup>st</sup> Ship Date:			Feb 19 2019			Estimated Sample Availability:			ple	Date pro	ovided at request.			
Chan	ae Tv	/pe:		Availability: Sample request.						•				
		bly Site									erials			
	Desigr			Electrical Specification						Mechanical Specification				
	est S								n		Test Process			
		Bump Site		Packing/Shipping/Labeling Wafer Bump Material				9	Ħ		er Bump Process			
		Fab Site				Fab Ma				$\overline{\Box}$		afer Fab Process		
	varei	Tab Site		=		umber c					ware	1 1 4 5 1 1 0		
						PCN D								
		f 61				PCN D	eta	115						
Desci	riptio	n of Change:												
	Texas Instruments is pleased to announce the qualification of additional Fab site (RFAB) and Assembly/Test site (AP1) options for the TLV6002 device family.  Additional Fab Site													
								0.00			1	Mafar		
		Fab Site	Pro	cess		afer meter	Fal	Site	Pr	ocess		Wafer ameter		
		DP1DM5	HF	PA07	20	0 mm	R	FAB	L	BC9	3	00 mm		
Assem	nbly c	onstruction di	fferen	ces are	as fo	ollows (		002IDF	R onl	y):		Addition	al	
N	lount	Compound						1147858				SID#101375281		
												SID#101380756		
	onu	wire compos	itioni	/diameter Au/0.96 mils				_			70.90 iiii Cu/1.0 m			
Lead Frame Finish				NiPdAu				DdΛιι				Matte Sn		
Lead Frame Finish				NIFUAU					Fidele					
Test coverage, insertions, conditions will remain consistent with current testing and verified with test MQ.  Upon expiration of this PCN, TI will combine lead free solutions in a single <u>standard part number</u> , for example; <u>TLV6002IDR</u> – can ship with both Matte Sn and NiPdAu.														
Reason for Change:														
		of Supply												
Antic	ipate	d impact on	Form	, Fit, F	unct	ion, Qu	ıality	or Re	liab	ility (	posit	tive / ne	gative):	
None														
Antic	ipate	d impact on	Mate	ial De	clara	ation								
	the	mpact to Material aration	Material Declarations or Product Content reports are driven from production data and will be available following the production release. Upon production release the revised reports can be obtained from the TLECO website.											

## Changes to product identification resulting from this PCN:

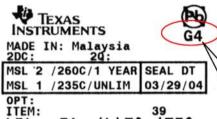
## **Fab Site Information:**

Chip Site	Chip Site Origin Code (20L)	Chip Site Country Code (21L)	Chip Site City
DP1DM5	DM5	USA	Dallas
RFAB	RFB	USA	Richardson

**Assembly Site Information:** 

Assembly Site	Assembly Site Origin (22L)	Assembly Country Code (21L)	Assembly City
TI Mexico	MEX	MEX	Aguascalientes
Amkor Phi	AKR	PHL	Cupang, Muntinlupa City

Sample product shipping label (not actual product label)



LBL: 5A (L)TO:3750



ECAT: G4 = NiPdAu ECAT: G3 = Matte Sn

(1P) SN74LS07NSR (D) 0336 31T)LOT: 3959047MLA 4W) TKY(1T) 7523483SI2

(2P) REV:

(20L) CSO: SHE (22L) ASO: MLA (23L) ACO: MYS

## **Product Affected:**

**Group 1 Device list (Additional RFAB Fab site qualification only):** 

TLV6002IDGKT TLV6002IDGKR

Group 2 Device list (Additional RFAB Fab and Assembly AP1 site Qualification):

TLV6002IDR



## Qualification Results

Data Displayed as: Number of lots / Total sample size / Total failed

Туре	Test Name / Condition	Duration	Qual Device: TLV6002IDGKR	QBS Product Reference: <u>TLV6002IDR</u>	QBS Process Reference: <u>TLV9062ID</u>	QBS Package Reference: TLV9062IDGK
ED	Electrical Characterization	Per Datasheet Parameters	Pass	Pass	Pass	Pass
ELFR	Early Life Failure Rate, 125C	48 Hours	-	-	3/2399/0	-
HAST	Biased HAST, 130C/85%RH	96 Hours	-	1/77/0	3/231/0	3/231/0
HBM	ESD - HBM	2000 V	-	1/3/0	3/9/0	-
CDM	ESD - CDM	1000 V	1/3/0	1/3/0	3/9/0	-
HTOL	Life Test, 150C	300 Hours	-	1/77/0	3/231/0	-
HTSL	High Temp Storage Bake 170C	420 Hours	-	1/77/0	3/231/0	3/230/0
LU	Latch-up	(per JESD78)	-	1/6/0	3/18/0	-
SD	Solderability	Pb Free	-	-	3/66/0	3/66/0
TC	Temperature Cycle, -65/150C	500 Cycles	1/77/0	1/76/0	3/231/0	3/231/0
UHAST	Unbiased HAST, 130C/85%RH	96 Hours	-	1/77/0	3/231/0	3/231/0

<sup>-</sup> QBS: Qual By Similarity

Quality and Environmental data is available at TI's external Web site: http://www.ti.com/ Green/Pb-free Status: Qualified Pb-Free(SMT) and Green

For questions regarding this notice, e-mails can be sent to the regional contacts shown below, or you can contact your local Field Sales Representative.

Location	E-Mail
USA	PCNAmericasContact@list.ti.com
Europe	PCNEuropeContact@list.ti.com
Asia Pacific	PCNAsiaContact@list.ti.com
Japan	PCNJapanContact@list.ti.com

<sup>-</sup> Qual Device TLV6002IDGKR is qualified at LEVEL2-260C
- Preconditioning was performed for Autoclave, Unbiased HAST, THB/Biased HAST, Temperature Cycle, Thermal Shock, and HTSL, as applicable
- The following are equivalent HTOL options based on an activation energy of 0.7eV: 125C/1k Hours, 140C/480 Hours, 150C/300 Hours, and 155C/240 Hours
- The following are equivalent HTSL options based on an activation energy of 0.7eV: 150C/1k Hours, and 170C/420 Hours
- The following are equivalent Temp Cycle options per JESD47: -55C/125C/700 Cycles and -65C/150C/500 Cycles